

Valeri Mladenov received his Ph.D. from the Technical University of Sofia (TU Sofia), Bulgaria in 1993. In 2019 he defenses a "Doctor of Sciences" thesis at the same institution. In 2004 he becomes the head of the department Fundamentals of Electrical Engineering. Since June 2011 he was a Dean of Faculty of Automatics, since Dec. 2011 he has been a Vice-Rector of TU Sofia. Currently, he is Head of the Department of Fundamentals of Electrical Engineering and Head of the Neurocomputing laboratory of TU-Sofia. He is a

guest lecturer at the Faculty of Electrical Engineering, Eindhoven University of Technology, in the Netherlands, and many others.

Prof. Mladenov's research interests are in the field of electrical engineering, artificial intelligence, power systems, electronics, neural networks. He has received many international research fellowships. He has more than 350 scientific papers in professional journals and conferences. He is a co-author of twenty books and manuals for students. He had received many research grants and also with his team he participated and participate in many national and international projects - H2020, FP7, Royal Society, DFG, NWO, Erasmus+, and others.

As a member of several editorial boards, Prof. Mladenov serves as an editor in chief, associate editor, and reviewer for a number of professional journals and conferences. He is a Senior Member of IEEE, a member of the IEEE Circuit and Systems Technical Committee (TC) on Cellular Nonlinear Networks and Memristor Array Computing (CNN-MAC), and an Educational Activities Officer of the Bulgarian IEEE section. He is also a Senior Member of the International Neural Networks Society (INNS), Member of the International Council of Large Electric Systems, (SIGRE), member of the Steering Committee of the International Symposium on Theoretical Electrical Engineering (ISTET), member of the Management Boards of the Scientific and Technical Union of the Power Engineers, and the Union of Automation and Informatics in Bulgaria.